

#512103
D. Wang
1/14/02

FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. BUR9-1999-0305US1	SERIAL NO. Not Assigned
	APPLICANTS: Toshiharu Furukawa, Mark C. Hakey, Steven J. Holmes, David V. Horak, James M. Leas, William H-L Ma, Paul A. Rabidoux	
	FILING DATE: Concurrently	GROUP: Not Assigned

U.S. Pat. No. 5,999,465
09/94465
08/31/01

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
Loke	4,851,889	7/25/89	Matsuzaki			
	5,374,571	12/20/94	Mukherjee et al.			
?	5,766,806	6/16/98	Spence			
	5,723,233	3/3/98	Garza et al.			
	5,766,829	6/16/98	Cathey, Jr. et al.			
	5,937,283	8/10/99	Lee			
	5,668,391	9/16/97	Kim et al.			
	5,831,329	11/3/98	Marso et al.			
	5,886,382	3/23/99	Witek			
✓	5,963,800	10/5/99	Augusto			
Loke	5,656,842	8/12/97	Iwamatsu et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

Loke	J.M. Hergenrother et al.; "The Vertical Replacement-Gate (VRG) MOSFET: A 50-nm Vertical MOSFET with Lithography-Independent Gate Length"; Bell Labs., Lucent Technologies
Loke	"Revolutionary Transistor Design Turns the Silicon World on End"; www.bell-labs.com/news/1999/november

EXAMINER

Loke

DATE CONSIDERED

10/19/02

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.